

758-370
843.37558VX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): WATANABE, et al.

Serial No.: Not Yet Assigned
(Divisional Application of SN 09/392,568
Filed: September 9, 1999)

Filed: July 12, 2001

For: METHOD OF MANUFACTURING A SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

JC903 U S PTO
09/902672
07/12/01

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & §1.98

Assistant Commissioner of Patents
Washington, D.C. 20231

July 12, 2001

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List, on a form substantially equivalent to Form PTO-1449, of documents cited in connection with prior application Serial No. 09/392,568, filed September 9, 1999, of which the present application is a Divisional application.

Since Serial No. 09/392,568 is being relied upon under 35 USC §120, copies of the listed documents are not enclosed. See 37 CFR §1.98(d).

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified Divisional application. Accordingly, clearly the requirements of 37 CFR §1.97(b) are satisfied.

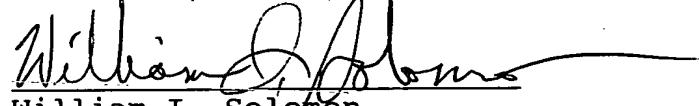
In view of the foregoing, it is respectfully submitted that all requirements of 37 CFR §1.97 and §1.98 have been satisfied, in connection with the listed documents. Accordingly, consideration of the listed documents, upon

examination of the above-identified application, is respectfully requested.

Please charge any shortage in fees due in connection with the filing of this paper to the Deposit Account No. 01-2135 (Case No. 843.37558VX1) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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